

**FILED UNDER 35 U.S.C. 371**

Class	Subclass
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**O.L.P.E.**

**PATENT DATE**

SCANNED

SA ④

6. W

APPLICATION NO.  
09/857022

CONT/PRIOR  
D F

CLASS  
7-10

**SUBCLASS**  
2-24

ART UNIT  
2128

EXAMINER Namoz

Tetsuya Ikeda  
Ryutaro Yamaneke

RSD

Interleave address generator device and interleave address generator

PTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL					CROSS REFERENCE(S)						
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION											

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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